

Substitute Form PTO-1449 (Modified)	U.S. Department of Commerce Patent and Trademark Office	Attorney's Docket No. 10559-535001	Application No. 09/989,483
<b>Information Disclosure Statement</b> <b>by Applicant</b> (Use several sheets if necessary)		Applicant <b>John A. Wishneusky</b>	
		Filing Date <b>November 19, 2001</b>	Group Art Unit <b>2182</b>

(37 CFR §1.98(b))

## U.S. Patent Documents

Examiner Initial	Desig. ID	Document Number	Publication Date	Patentee	Class	Subclass	Filing Date If Appropriate
	AA						
	AB						
	AC						
	AD						
	AE						
	AF						
	AG						
	AH						
	AI						
	AJ						
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## Foreign Patent Documents or Published Foreign Patent Applications

Examiner Initial	Desig. ID	Document Number	Publication Date	Country or Patent Office	Class	Subclass	Translation	
							Yes	No
	AL							
	AM							
	AN							
	AO							
	AP							

## Other Documents (include Author, Title, Date, and Place of Publication)

Examiner Initial	Desig. ID	Document
CSK	AQ	Li, Yamin, et al., "The Effects of STEF in Finely Parallel Multithread Processors", <i>IEEE International Symposium on High-Performance Computer Architecture</i> , pp. 318-325, January 1995.
CSK	AR	Loikkanen, et al., "A Fine-Grain Multithreading Superscalar Architecture", <i>IEEE Comp. Soc.</i> , pp.163-168, October 1996.
CSK	AS	Thistle, et al., "A Processor Architecture for Horizon", <i>IEEE Proceedings Supercomputing</i> , pp. 35-41, November 1988.
	AT	

Examiner Signature <i>Ken S. Kim</i>	Date Considered <i>8-20-04</i>
EXAMINER: Initials citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.	

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